

Abstract of the Disclosure

The present video apparatus comprises: a device for generating a video signal; a memory including a storage region for storing a program and a storage region for storing a data of an analysis; an internal analysis processing circuit for an image analysis from the video signal according to the procedure of the image analyzing program to output a video signal of the image analysis to an external unit; an addressing circuit for assigning an address in the video signal from the result of the image analysis; and a comparison circuit for comparing the video signal assigned the address to a video signal of an image set up by the program to output the video signal to the internal analysis processing circuit if both of the video signals are the same.

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